## Approved For Belease 2002/06/17 : CIA-RDP78B0474 001500030034-6

NPIC/P&DS/D/6-1655 28 October 1966

	MEMORANDUM FOR THE RECORD	
25X1A	SUBJECT: Trip Report to To Inspect Chip Comparator	
25X1A	1. On 26 October 1966 and myself visited the The purpose of the visit was to ascertain what steps had been taken to isolate and correct Chip	25X1A
25X1A	Comparator interferometer drift.	
25X1A	2, the president of the company, described their analysis and solution. The necessary improvements have been incorporated in the interferometer borrowed from Chip Comparator 405B, Serial No. 1 (IAD).	
25X1A	According to, they have obtained reliable operation over a temperature range of 65 to 90 degrees for a period of three weeks. His feeling is that they have solved the problem.	
25X1A	3. suggested that the other interferometer and the two digitizer chassis from 405B Serial No. 1, be installed on the 405A, which is currently being converted to the 405B configuration. It would then be run approximately 1 month to check out the complete system. sqreed to this, and a representative will pick up the components during the first week in November. If everything is accomplished on schedule the modified 405A could be delivered in early January, 1967. The first 405B system should be operational by April, and the others at two week intervals thereafter.	25X1A
		05)///
	Equipment Performance Section, Page 1	25X1A LDS

Distribution
Orig---P&DS/DB/EPS
1 ---P&DS/DB

**DECLASS REVIEW by NIMA/DOD** 

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